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Bib Data Sheet

CONFIRMATION NO. 8429

SERIAL NUMBER 10/733,034	FILING DATE 12/11/2003 RULE	CLASS 438	GROUP ART UNIT 1765	ATTORNEY DOCKET NO. 01-916/1D
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APPLICANTS

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** CONTINUING DATA *****

This application is a DIV of 10/185,537 07/01/2002 PAT 6,699,766

** FOREIGN APPLICATIONS *****

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 03/16/2004

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged Examiner's Signature: [Signature] Initials: [Initials]	OR	7	5	1

ADDRESS

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TITLE

Method of fabricating an integral capacitor and gate transistor having nitride and oxide polish st layers using chemical mechanical polishing elimination

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